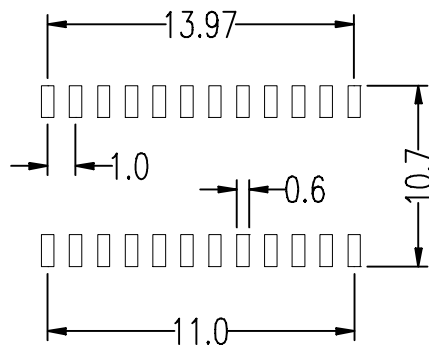
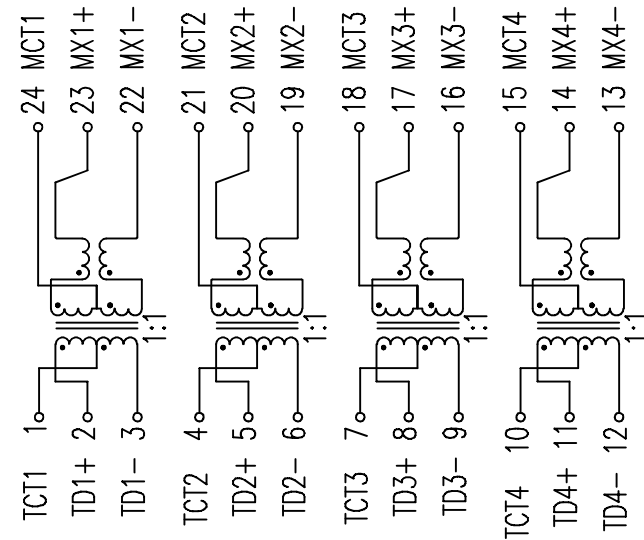


UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE  $\pm 0.25$



PCB SUGGEST PAD LAYOUT



ELECTRICAL CHARACTERISTICS :

1. TURNS RATIO : TX = 1CT : 1CT  $\pm 5\%$   
RX = 1CT : 1CT  $\pm 5\%$
2. INSERTION LOSS : 1-100MHz = -1.0dB MAX.
3. CROSS TALK : 30MHz = -43dB MIN.  
60MHz = -37dB MIN.  
100MHz = -33dB MIN.
4. RETURN LOSS (@LOAD 100 $\Omega$ ) :  
30MHz = -18dB MIN.  
60MHz = -12dB MIN.  
100MHz = -10dB MIN.
5. DCMRR : 30MHz = -43dB MIN.  
60MHz = -37dB MIN.  
100MHz = -33dB MIN.
6. HI-POT : 1500Vrms
7. LK (@100kHz, 0.1Vrms) : 0.3uH MAX.
8. Cw/w (@100kHz, 0.1Vrms) : 25pF MAX.
9. Suitable for End-span PoE Application with 700mA Current capability
10. RoHS COMPLIANT

								TITLE 10/100/1000 BASE-T PoE TRANSFORMER	
								DWG. NO. ATPL-485R	
RELEASE						UNITS:		SAFETY	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE		P/N:	
REVISIONS								SHEET 1 of 1	
								DRAW	